

LA203-50

OPTO INTERRUPTER



Features

- Non-contact switching.
- For direct PC board or dual-in-line socket mounting.
- Fast switching speed.

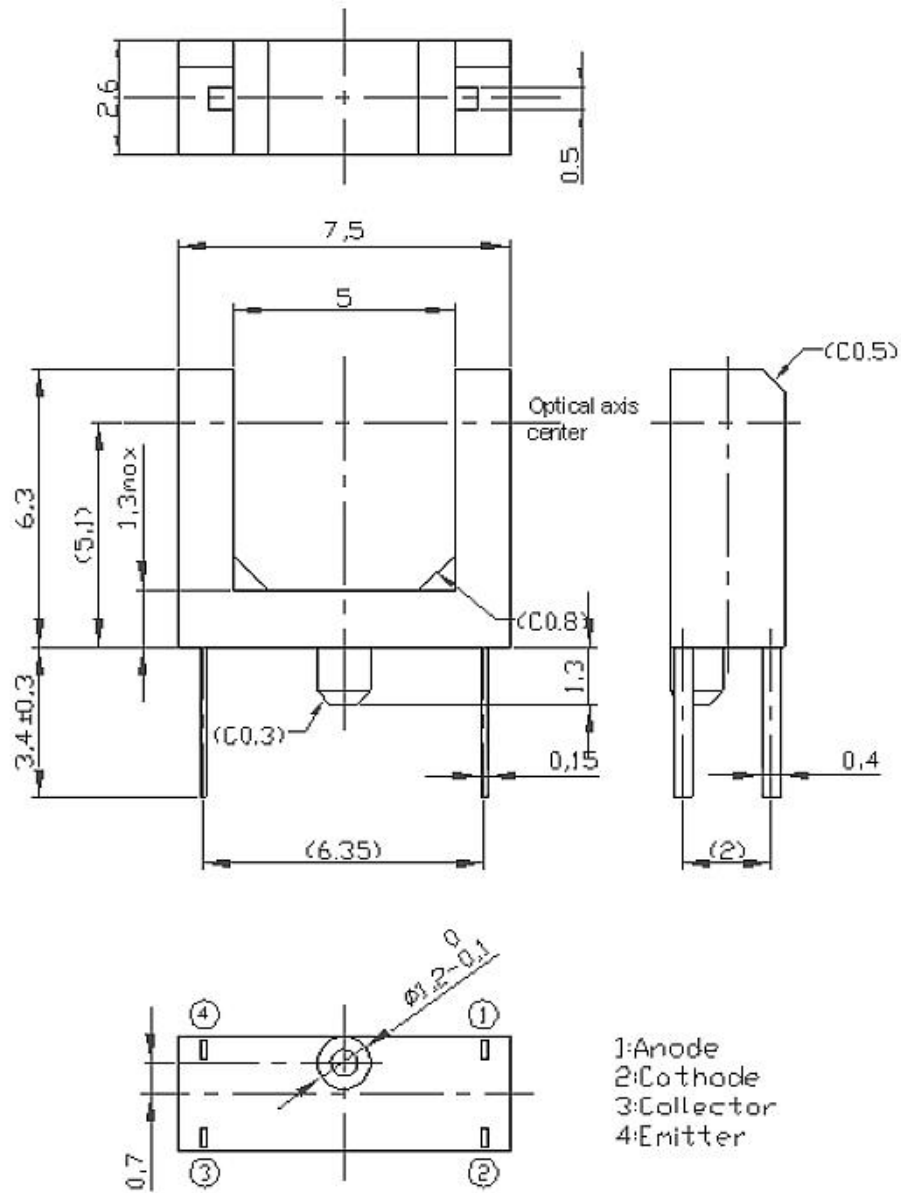
Application

- Scanner
- Printer
- FAX machine
- Counter

Description

The LA203-50 series consist of Gallium Arsenide infrared emitting diode and a NPN silicon phototransistor mounted in a black plastic housing. Phototransistor switching takes place whenever an opaque object passes through the slot. These series are designed for direct soldering into PC board or mounting in standard dual-in-line socket.

PACKAGE DIMENSIONS



NOTES:

1. All dimensions are in millimeters (inches).
2. Tolerance is ±0.25mm(.010") unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.

ABSOLUTE MAXIMUM RATINGS AT TA=25°C

PARAMETER	MAXIMUM RATING	UNIT
IR Diode Continuous Forward Current	50	mA
IR Diode Reverse Voltage	5	V
Transistor Collector Current	20	mA
Transistor Power Dissipation	100	mW
IR Diode Peak Power Current (Pulse Wide = 1μS, 300 pps)	3	A
Diode Power Dissipation	175	mW
Phototransistor Collector-Emitter Voltage	30	V
Phototransistor Emitter-Collector Voltage	5	V
Operating Temperature Range	-40°C to + 85°C	
Storage Temperature Range	-50°C to + 100°C	

ELECTRICAL OPTICAL CHARACTERISTICS AT TA=25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
INPUT LED						
Forward Voltage	V _F		1.2	1.35	V	I _F = 20mA
Reverse Current	I _R			100	μA	V _R =5V
OUTPUT PHOTOTRANSISTOR						
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	30			V	I _C =1mA
Emitter-Collector Breakdown Voltage	V _{(BR)CEO}	5			V	I _E =0.1mA
Collector-Emitter Dark Current	I _{CEO}			100	nA	V _{CE} =10V
COUPLER						
Collector-Emitter Saturation Voltage	V _{CE(SAT)}			0.4	V	I _C =0.2mA I _F =20mA
Current Transfer Ratio	I _{c(on)}	0.8			mA	V _{CE} =5V I _F =20mA

TYPICAL ELECTRICAL / OPTICAL CHARACTERISTICS CURVES

(25°C Ambient Temperature Unless Otherwise Noted)

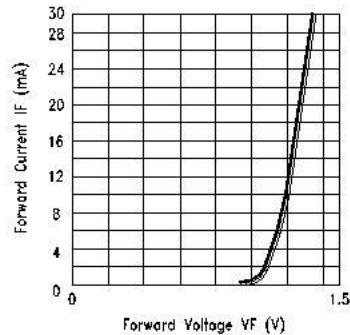


Fig.1 FORWARD CURRENT VS. FORWARD VOLTAGE

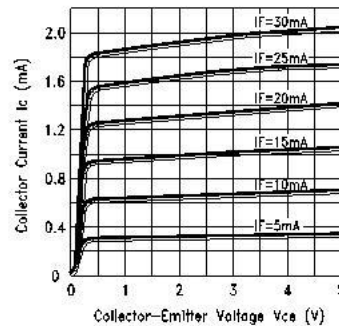


Fig.2 COLLECTOR CURRENT VS. COLLECTOR VOLTAGE

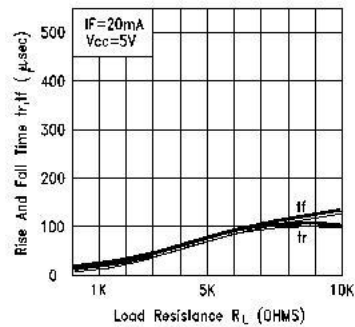


Fig.3 RISE AND FALL TIME VS. LOAD RESISTANCE

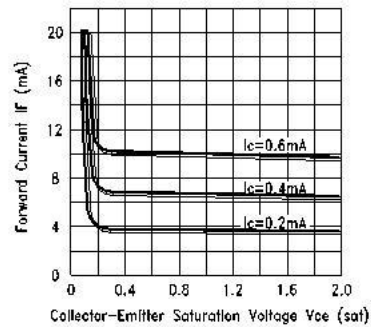


Fig.4 FORWARD CURRENT VS. COLLECTOR-EMITTER SATURATION VOLTAGE

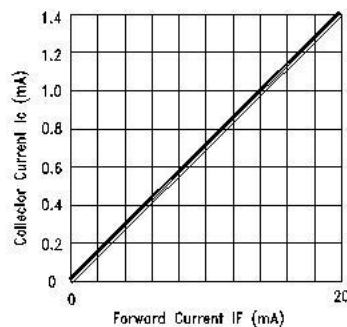


Fig.5 COLLECTOR CURRENT V.S FORWARD CURRENT

Notes

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